PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Hiroyuki YASHIRO, Kinji MORI and Scott A. MCDERMOTT

Application No.: New US Patent Application

Filed: April 18, 2001 Docket No.: 109273

For: REDUNDANT INFORMATION PROCESSING SYSTEM

PRELIMINARY AMENDMENT

Director of the U.S. Patent and Trademark Office Washington, D. C. 20231

Sir:

Prior to initial examination, please amend the above-identified application as follows: IN THE CLAIMS:

Please replace claims 4-5 as follows:

4. (Amended) The redundant information processing system according to claim 2 wherein, in the case where said processing device uses past control information in the process of generating said control information,

said processing device other than processing devices with valid control information rewrites the generated control information to control information of the processing device with valid control information, and uses the rewritten control information for the next process of generating control information.

5. (Amended) The redundant information processing system according to claim 1 wherein the processing procedures of the logical decision made by said transmission permitting parts of said three or more processing devices are common to one another.

Please add new claims 6-9 as follows:

--6. The redundant information processing system according to claim 3 wherein, in the case where said processing device uses past control information in the process of generating said control information,

said processing device other than processing devices with valid control information rewrites the generated control information to control information of the processing device with valid control information, and uses the rewritten control information for the next process of generating control information.--

- --7. The redundant information processing system according to claim 2 wherein the processing procedures of the logical decision made by said transmission permitting parts of said three or more processing devices are common to one another.--
- --8. The redundant information processing system according to claim 3 wherein the processing procedures of the logical decision made by said transmission permitting parts of said three or more processing devices are common to one another.--
- --9. The redundant information processing system according to claim 4 wherein the processing procedures of the logical decision made by said transmission permitting parts of said three or more processing devices are common to one another.—

REMARKS

Claims 1-9 are pending. Claims 4-5 are amended and claims 6-9 are added to eliminate multiple dependencies. Prompt and favorable consideration on the merits is respectfully solicited.

The attached Appendix includes marked-up copies of each rewritten claim (37 C.F.R. 1.121(c)(ii)).

Respectfully submitted,

Registration No. 27,075

Joel S. Armstrong Registration No. 36,430

JAO:JSA/zmc

Attached:

APPENDIX

Date: April 18, 2001

OLIFF & BERRIDGE, PLC P.O. Box 19928 Alexandria, Virginia 22320 Telephone: (703) 836-6400 DEPOSIT ACCOUNT USE
AUTHORIZATION
Please grant any extension
necessary for entry;
Charge any fee due to our
Deposit Account No. 15-0461

APPENDIX

Changes to Claims:

Claims 6-9 are added.

The following are marked-up versions of the amended claims:

4. (Amended) The redundant information processing system according to claim 2 or 3 wherein, in the case where said processing device uses past control information in the process of generating said control information,

said processing device other than processing devices with valid control information rewrites the generated control information to control information of the processing device with valid control information, and uses the rewritten control information for the next process of generating control information.

5. (Amended) The redundant information processing system according to <u>claim</u>

<u>lany of claims 1 or 4</u> wherein the processing procedures of the logical decision made by said transmission permitting parts of said three or more processing devices are common to one another.